

ABSTRACT

A system and method for providing communication between two computer bus segments. Two half-bridge circuits are connected to respective computer bus segments and frame data for a high speed transfer of the data between the half-bridge circuits. A common application is to connect multiple PCI segments that are individually limited to the number of components connected thereto. Connection through the high speed half-bridge circuits allows additional PCI components to communicate on a second computer bus segment, acting as an expansion bus.

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